



## ABOUT ME

Accomplished hardware developer with strong experience in collaborating with teams to tackle complex challenges, evaluate design trade-offs, and deliver solutions that exceed client expectations. Detail-oriented and proactive, committed to building reliable and innovative embedded systems.

## INTERESTS

Learning · Lifting weights · TradFi · DeFi · 3D printing · 3D modeling · Microcontrollers · Programming

# Bc. JAKUB URBÁNEK

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## HARDWARE DEVELOPER

### WORK EXPERIENCE

October 2024 (present); Blansko, Czechia

#### HARDWARE DEVELOPER – TRAMECo Systems, s.r.o.

Designing, prototyping, and testing electronic circuits and systems for railway industry. Schematic design, PCB layout, and hardware debugging. Working with microcontrollers, digital and analog components, and supporting product development from concept to mass production. Collaborating with cross-functional teams to deliver reliable and innovative hardware solutions.

August 2023 – August 2024; Brno, Czechia

#### EMC TEST ENGINEER – UNIS, a.s.

Conducting compliance testing in Semi-Anechoic Chambers (SAC). Measuring electromagnetic emissions and immunity of electronic devices, preparing test setups according to international standards (CISPR, IEC, ISO), and analyzing results to ensure regulatory compliance.

June 2021 – March 2022; Brno, Czechia

#### 3D MODELING OF HUMAN JAWS – TESCAN 3DIM, S.R.O.

Creating 3D models based on CT scans of anonymous patients and segmenting models according to international tooth numbering system. Correcting computer-generated models.

### EDUCATION

#### BACHELOR'S DEGREE

#### MICROELECTRONICS AND TECHNOLOGY – FEKT, VUT Brno

### CURRENT STUDIES

#### MASTER'S DEGREE

#### ELECTRICAL PRODUCTION AND MANAGEMENT – FEKT, VUT Brno